

MCV 16 2000

Sheet: 1 of: 1

FORM: PTO-1449
(REV: 7-80)
PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(37 CFR 1.98(b))

(use several sheets if necessary)

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Applicant:
Akram, et al.

Filing Date: Group:
8/31/00

U.S. PATENT DOCUMENTS

Exammer		Document					
Initial>		Number	Date	Name	Class	Subclass	
1 /1	AA	5,962,921	10/5/99	Farnworth et al.	257	773	
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FOREIGN PATENT DOCUMENTS

Examiner	Document	t				Trans	lation
lnitial	Number	Date	Country	Class	Subclass	Yes	No
	AC	I					

Initial	OTHER ART (including author, title, date, pertinent pages, etc.)
AI AI	"Condel Test Socket Specifications," Liberty Research Socket Specifications, Web Page: www.liberty-
	research.com/sktspcs.htm; February 10, 1998.
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Al	"Holeshot Test Socket and Adapter for Area Array Packages," Liberty Research Web Page: www.liberty-research.com/holeshot.htm; February 10, 1998.
Al	"Penta O Probe Sheet," Interconnect Devices, Inc., catalog page.
A	"Plunger Tip Styles by Size," Catalog page from Interconnect Devices, Inc.
Al	"Probe Plunger Tip Application Guide," Interconnect Devices, Inc., Web Page: www.idinet.com/aguide.htm; December 20, 1998.
AJ	"Series 654 Ball Grid Array Socket (on 1.27 mm pitch); catalog page, Wells Electronics, Inc., South Bend, Indiana.
A	"Series 654 Ball Grid Array Socket (on 1.5 mm pitch); catalog page, Wells Electronics, Inc., South Bend, Indiana.
AX	"Series 655 OTBGA Burn-In/Test Socket (Pinch Contact)," Wells-CTI, Web Page: www.wellscti.com/ser655p.htm, December 16, 1998.
AL	"Series 655 OTBGA Socket (Y Contact)," Wells-CTI, Web Page: www.wellscti.com/ser655.htm ; December 16, 1998.
AN	"Test Sockets and Adapters for Area Array Packages," Liberty Research, Web Page: www.liberty-research.com/arayinfo.htm; February 10, 1998.
Examiner:	Date Considered:

EXAMINER:

Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication with applicant.

Docket Number (Optional) Application Nurl Form PT 1449 5095.5US (97-1102.05/US) 09/652,685 TNFORMATION DISCLOSURE CITATION Applicant Akram et al. IN AN APPLICATION (Use several sheets if necessary) Filing Date August 31, 2000 Group Art Unit 3729 **U.S. PATENT DOCUMENTS** DOCUMENT NUMBER FILING DATE IF APPROPRIATE **EXAMINER** SUBCLASS DATE NAME CLASS INITIAL 5,741,741 04/1998 Tseng 04/2000 6,049,977 **Atkins** 6,092,282 07/2000 Roberts 6,397,460 06/2002 Hembree TECHNOLOGY CENTER R3700 FOREIGN PATENT DOCUMENTS Translation DOCUMENT NUMBER DATE COUNTRY CLASS SUBCLASS YES **OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.) Smith, "Circuits, Devices and Systems" Fourth Edition, 1984. EXAMINER **DATE CONSIDERED** EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if

not in conformance and not considered. Include copy of this form with next communication to the applicant.